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APPLICANTS

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** CONTINUING DATA *****
 This application is a DIV of 09/882,892 06/15/2001 PAT 6,740,942 ✓ JN

** FOREIGN APPLICATIONS ***** *now* ✓ JN

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 05/19/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>JOSEPH W. VIGEN</i> Examiner's Signature Initials	STATE OR COUNTRY CA	SHEETS DRAWING 3	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 1
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ADDRESS

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TITLE

Permanently on transistor implemented using a double polysilicon layer CMOS process with buried contact

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